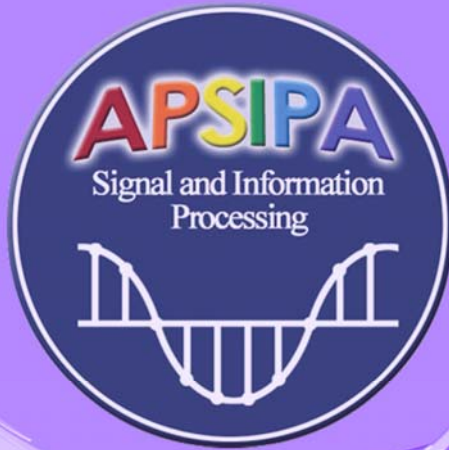


Issue 13

Oct 2016



APSIPA
NEWSLETTER
Asia-Pacific Signal and Information Processing Association Newsletter

Greetings!

The APSIPA Newsletter Issue 13 starts with a good news . APSIPA Transactions on Signal and Information Processing (ATSIP) has been accepted for inclusion in the Emerging Sources Citation Index (ESCI). This is certainly a piece of encouraging news! Members are all invited to submit your manuscript at <http://mc.manuscriptcentral.com/apsipa>.



Besides, 2016 APSIPA Industrial Distinguished Leaders has recently been announced. The five Distinguished Leaders are:

- Bo Begole, VP, Global Head of Media Technologies Lab, Huawei R&D, USA
- Achin Bhowmik, Vice President and General Manager, Perceptual Computing Group, Intel Corporation
- Chih-Lin I, Chief Scientist of Wireless Technologies, China Mobile Research Institute (CMCC)
- Takehiro Moriya, Research Fellow & Director, NTT Fellow, Moriya Research lab
- Anthony Vetro, Director, Mitsubishi Electrical Research Laboratories, USA

Congratulations!

Contributions from members are always highly appreciated. All members are invited to send us your contributions to publish in APSIPA Newsletter. Don't forget to give us thoughts about the further development of APSIPA Newsletter. Enjoy reading this issue!

Bonnie Law

APSIPA Newsletter EiC

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GOOD NEWS!

APSIPA Transactions on Signal and Information Processing (ATSIP)

ATSIP is an open-access e-only journal of APSIPA. It has now been accepted for inclusion in the Emerging Sources Citation Index (ESCI). This is a new database in the Web of Science Core Collection from Thomson Reuters. ESCI complements other Thomson Reuters indexes by providing earlier visibility for sources under evaluation as part of the Science Citation Index (SCI). We are still awaiting an answer on our initial application made to the SCI and we will try to obtain an answer from Thomson Reuters as soon as possible. Just to be clear, inclusion in the ESCI does not mean that the journal is included in the Journal Citation Reports (JCR) - the database that leads to a journal being given an impact factor. What it does mean, however, is that the journal is now more discoverable, which will hopefully lead to more citations, thus strengthening our case for inclusion in the SCI and JCR. ATSIIP papers published from 2016 onwards will now be included in the ESCI.

More information on the ESCI can be found here:

http://wokinfo.com/products_tools/multidisciplinary/esci/

Information about ATSIIP

ATSIP serves as an international forum for signal and information processing researchers across a broad spectrum of research, ranging from traditional modalities of signal processing to emerging areas where either (i) processing reaches higher semantic levels (e.g., from speech recognition to multimodal human behaviour recognition) or (ii) processing is meant to extract information from datasets that are not traditionally considered signals (e.g., mining of Internet or sensor information).

Merit of this journal:

- Open access: free, permanent, worldwide access to your article
- Rapid publication: continuous publication of articles as soon as they are accepted
- Peer reviewed by international experts, first review response available in less than 2 months
- Free color, no over-length page charges (flat \$600 processing fee)
- Papers indexed by Scopus
- Papers linked on Google Scholar

Express White Papers:

Express White Papers makes it easier for authors of accepted conference papers to convert their submissions into journal paper submissions. The white paper model, in use in some well known publications in our field, makes it easier for authors to get an assessment of whether their planned

submission has a good chance to be accepted. Because the review is based on a conference paper and a white paper, which are both short, it should be possible to provide this feedback quickly to the authors. This initial work will also make it more likely that the full paper submission (if recommended) will be processed faster.

APSIPA Sadaoki Furui Prize Paper Award

APSIPA Sadaoki Furui Prize Paper Award will be awarded at APSIPA ASC each year based on selection from the papers published in the preceding five years.

For more details, *please visit* <http://journals.cambridge.org/sip>

Submit your manuscript today: <http://mc.manuscriptcentral.com/apsipa>

APSIPA Industrial Activities

One important activity in APSIPA is industrial activities. Dr Shan Liu is the current Vice President of Industrial Relations and Development of APSIPA. The composition of the APSIPA Industrial Governance Board is as follows:

Chair - VP of Industrial Relations and Development

[Shan Liu](#), MediaTek, USA (Chair)

Past-Chair - VP of Industrial Relations and Development

[Haohong Wang](#), TCL Research America, USA

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[Tong Zhang](#), HP Labs, USA (Past-Chair)
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[Khaled El-Maleh](#), Qualcomm, USA
[José Roberto Alvarez](#), Futurewei Technologies, USA
[Jin-Gyeong Kim](#), LG, Korea
[Anil Kokaram](#), YouTube/Google, USA

Industrial Membership Committee

[Shinji Watanabe](#), MERL, USA (Chair)
[Shan Liu](#), MediaTek, USA (Past-Chair)
[Ying Li](#), IBM, USA
[Masami Akamine](#), Toshiba, Japan
[Takafumi Koshinaka](#), NEC, Japan
[Jim Zhao](#), Huawei, USA
[Yan Ye](#), InterDigital, USA
[Hiroshi Sawada](#), NTT Communication Science Labs, Japan
[Michiel Bachiani](#), Google, USA

For more information about APSIPA Industrial Activities, please visit <http://www.apsipa.org/industrial.htm>.

Industrial Distinguished Leader

The APSIPA Industrial Distinguished Leader is a distinction reserved for selected APSIPA members (or potential members) whose extra-ordinary accomplishments in any of the fields related to APSIPA scope are deemed fitting of this prestigious recognition. The nomination and selection process are conducted twice a year. Each nominee is discussed and voted within each Industrial Committee; selected candidates are then recommended to the Industrial Governance Board for final approval. The guideline about nomination of the Industrial Distinguished Leader is available at

http://www.apsipa.org/doc/APSIPA_industrial_leader_nomination_guideline.pdf

This year, we have **five** newly selected APSIPA Industrial Distinguished Leaders.

Bo Begole

VP, Global Head of Media Technologies Lab, Huawei R&D USA

Dr. Bo Begole is VP and Global Head of Huawei Technologies' Media Technologies Lab spanning locations in China, US, Europe and Russia to create the future of networked media technologies and user experiences including ultra-high-efficiency compression, computer vision/hearing, augmented/virtual reality, full field communications and personalized, responsive media. Previously, he was a Sr. Director at Samsung Electronics' User Experience Center America where he directed a team to develop new contextually intelligent services for wearable, mobile and display devices. Prior to that, he was a Principal Scientist and Area Manager at Xerox PARC where he directed the Ubiquitous Computing research program creating behavior-modeling technologies, responsive media and intelligent mobile agents. He is the author of *Ubiquitous Computing for Business* (FT Press, 2011) and dozens of peer-reviewed research papers. He is an inventor with 30 issued patents and dozens pending. Dr. Begole is an ACM Distinguished Scientist, active in many research conferences and was recently co-Chair of the 2015 ACM conference on human factors in computing systems (CHI 2015) in Seoul, Korea. His teams have developed core technologies for products in video compression, unified communication, groupware, media interoperability, consumer electronics and mobile contextually intelligent agents.



Dr. Begole received a Ph.D. in computer science from Virginia Tech in 1998.

Achin Bhowmik

Vice President and General Manager, Perceptual Computing Group,
Intel Corporation, www.intel.com/realsense

Dr. Achin Bhowmik is vice president and general manager of the Perceptual Computing Group at Intel Corporation. He leads the development and deployment of advanced computing solutions based on natural sensing and interactions, branded as Intel® RealSense Technology. His responsibilities include creating and growing new businesses in the areas of interactive computing systems, immersive virtual reality devices, autonomous robots and unmanned aviation systems.



Previously, he served as the chief of staff of the personal computing group, Intel's largest business unit with over \$30B revenues. Prior to that, he led the development of advanced video and display processing technologies for Intel's computing products. His prior work includes liquid-crystal-on-silicon microdisplay technology and integrated electro-optical devices.

As an adjunct and guest professor, Dr. Bhowmik has advised graduate research and taught human-computer interactions, computer vision and display technologies at Stanford University, University of California, Berkeley, Liquid Crystal Institute of the Kent State University, Kyung Hee University, Seoul, and the Indian Institute of Technology, Gandhinagar. He holds a B.Tech. from the Indian Institute of Technology, Kanpur, a Ph.D. from Auburn University, and attended the executive program at Stanford University.

Dr. Bhowmik is a Fellow of the Society for Information Display (SID), and serves on the executive committee of SID. He has over 100 publications, including two books titled “Interactive Displays: Natural Human-Interface Technologies” and “Mobile Displays: Technology & Applications”, and over 100 granted and pending patents. He is an associate editor for the Journal of the Society for Information Display. He is on the board of directors for OpenCV, the organization behind the open source computer vision library.

Chih-Lin I

Chief Scientist of Wireless Technologies, China Mobile Research Institute, CMCC

Dr. Chih-Lin I is the China Mobile Chief Scientist of Wireless Technologies, in charge of advanced wireless communication R&D effort of China Mobile Research Institute (CMRI). She established the Green Communications Research Center of China Mobile, spearheading major initiatives including 5G Key Technologies R&D; high energy efficiency system architecture, technologies, and devices; green energy; C-RAN and soft base station.



Dr. I received her Ph.D. degree in Electrical Engineering from Stanford University, has almost 30 years experience in wireless communication area. She has worked in various world-class companies and research institutes, including wireless communication fundamental research department of AT&T Bell Labs; Headquarter of AT&T, as Director of Wireless Communications Infrastructure and Access Technology; ITRI of Taiwan, as Director of Wireless Communication Technology; Hong Kong ASTRI, as VP and the Founding GD of Communications Technology Domain.

Dr. I received the Trans. COM Stephen Rice Best Paper Award, and is a winner of CCCP “National 1000 talent” program. She was an elected Board Member of IEEE ComSoc, Chair of ComSoc Meeting and Conference Board, and the Founding Chair of IEEE WCNC Steering Committee. She is currently an Executive Board Member of GreenTouch, a Founding Member of ETSI NFV Network Operator Council, a Steering Board Member of WWRF, and a Scientific Advisory Board Member of Singapore National Research Foundation. She won the 2015 Industrial Innovation Award of IEEE Communication Society for leadership and innovation in next-generation cellular wireless networks.

Takehiro Moriya

Research Fellow & Director, NTT Fellow, Moriya Research Lab

Takehiro Moriya received the B.S., M.S., and Ph.D. degrees in Mathematical Engineering and Instrumentation physics from the University of Tokyo, in 1978, 1980, and 1989, respectively. Since joining NTT Laboratories in 1980, he has been engaged in research on medium to low bit rate speech and audio coding. In 1989, he worked at AT&T Bell Laboratories as a Visiting Researcher.

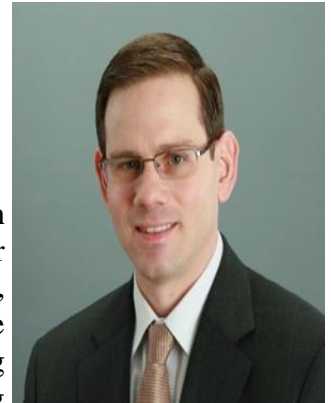


Since 1990 he has contributed to the standardization of coding schemes for the Japanese Public Digital Cellular System, ITU-T, ISO/IEC MPEG, and 3GPP. He is currently Head of the Moriya Research Laboratory in NTT Communication Science Laboratories, and is NTT Fellow, IEEE Fellow, and Institute of Electronics, Information and Communication Engineers (IEICE) Fellow. He is a member of the IEEE Speech Technical Committee of the Signal Processing Society (SPS), and a chair of the IEEE SPS Tokyo Joint Chapter. He received number of awards including IEEE James L. Flanagan Speech and Audio Processing Award in 2016.

Anthony Vetro

Director, Mitsubishi Electrical Research Laboratories, IEEE fellow

Anthony Vetro is a Deputy Director at Mitsubishi Electric Research Labs, in Cambridge, Massachusetts. He also manages a group that is responsible for research in the areas of visual coding and analysis, information security, computational sensing, and speech/audio processing. He has contributed to the transfer and development of several technologies to Mitsubishi products, including digital television receivers and displays, surveillance and camera monitoring systems, automotive equipment, as well as satellite imaging systems.



Anthony has published more than 200 papers and has been an active member of the MPEG and ITU-T video coding standardization committees for a number of years. He has served as editor and ad-hoc chair for several projects, including the Multiview Video Coding standard. From 2011-2014, he served as Head of the US Delegation to MPEG. Since 2015, he has been serving as the Chair of the US Technical Advisory Group to ISO/IEC JTC 1/SC 29 (INCITS L3).

He is also active in various IEEE conferences, technical committees and editorial boards. Current or past roles include Senior Editorial Board of IEEE Journal on Selected Topics in Signal Processing and IEEE Journal on Emerging and Selected Topics in Circuits and Systems, Editorial Board of IEEE Signal Processing Magazine and IEEE Multimedia, Associate Editor of IEEE Transactions on Circuits and Systems for Video Technology and IEEE Transactions on Image Processing, Chair of TC on Multimedia Signal Processing of the IEEE Signal Processing Society, and Steering Committee of IEEE Transactions on Multimedia.

Anthony received the B.S., M.S. and Ph.D. degrees in Electrical Engineering from Polytechnic University, in Brooklyn, NY. He has received several awards for his work on transcoding and is a Fellow of the IEEE.

Summary of Links

- APSIPA ASC 2016: <http://www.apsipa2016.org>
- APSIPA Friend's Lab: <http://www.apsipa.org/friendlab/FriendLabs.htm>.
- APSIPA Transaction on Signal and Information Processing: <http://journals.cambridge.org/sip>
- Paper Submission to APSIPA Transaction on Signal and Information Processing:
<http://mc.manuscriptcentral.com/apsipa>
- Information about Emerging Sources Citation Index (ESCI):
http://wokinfo.com/products_tools/multidisciplinary/esci/
- APSIPA Industrial Activities: <http://www.apsipa.org/industrial.htm>
- Guideline for Industrial Distinguished Leaders Nomination:
http://www.apsipa.org/doc/APSIPA_industrial_leader_nomination_guideline.pdf
- APSIPA Membership Registration/Renewal: <http://www.apsipa.org/reg.asp>

Calendar of Events

- APSIPA ASC 2016: <http://www.apsipa2016.org>
 - 14 Oct 2016: Camera-Ready Papers Submission and Author Registration deadline
 - For information, please visit: http://www.apsipa2016.org/final_paper_submission.do
 - 13 Dec 2016, 2:00—5:30pm: Tutorial Session
 - 2 Tutorials:
 - Recent R&D Trends and outlook on Brain-computer/machine Interface (BCI/BMI) Technique, Professor Sung Chan Jun, Gwangju Institute of Science and Technology, South Korea.
 - Multi-Microphone Speech Recognition, Dr Shinji Watanabe, Mitsubishi Electric Research Laboratories (MERL), USA.
 - For further information, please visit: <http://www.apsipa2016.org/tutorial.do>
 - 14 Dec 2016—16 Dec 2016: Summit and Conference Dates
 - Keynote Speakers:
 - Professor In So Kweon, KAIST, Korea, School of Electrical Engineering, National Core Research Center, Korea.
Title: How did “DRC-HUBO+” win the DARPA Robotics Challenge? - Robust Computer Vision Algorithms for “DRC-HUBO+”.
 - Professor Shigeki Sagayama, Fellow of Institute of Electronic, Information and Communications Engineers of Japan, Meiji University, Japan.
Title: Fun with Music Processing Research
 - Dr Andrew Woods, Centre for Marine Science & Technology, HIVE Visualisation Facility, Curtin University, Australia.
Title: Two Historic Shipwrecks - 3D Imaging Survey and 3D Reconstruction Processing
 - For further information, http://www.apsipa2016.org/keynote_speakers.do

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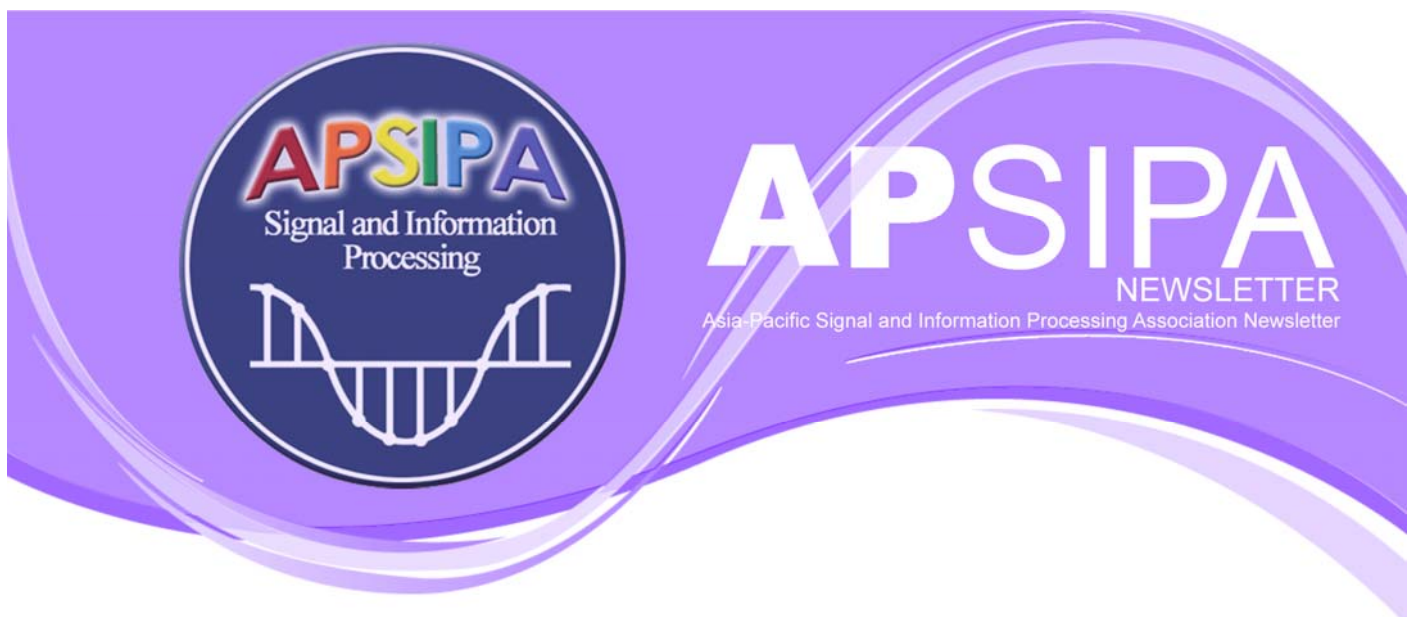
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<http://www.apsipa.org>

More members to come!